21

outputting from the programmable-matched filter, during clock cycle two-hundred and two-hundred-one, an output due to the fourth signal;

loading, at clock cycle two-hundred-five, the programmable-matched filter with a reference corresponding to a fifth signal;

22

outputting from the programmable-matched filter, during clock cycle two-hundred-fifty and two-hundred-fifty-one, an output due to the fifth signal.

\* \* \* \* \*